7

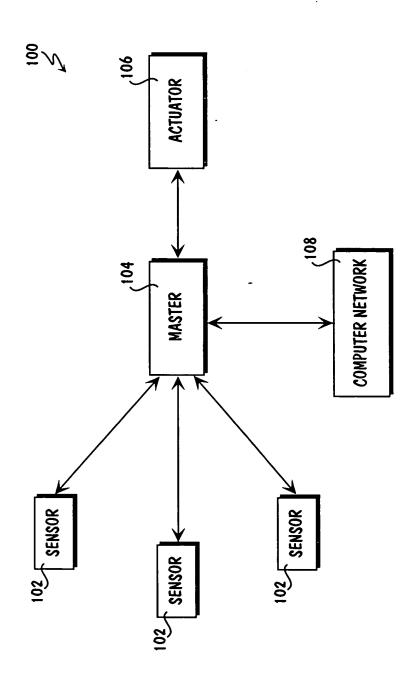
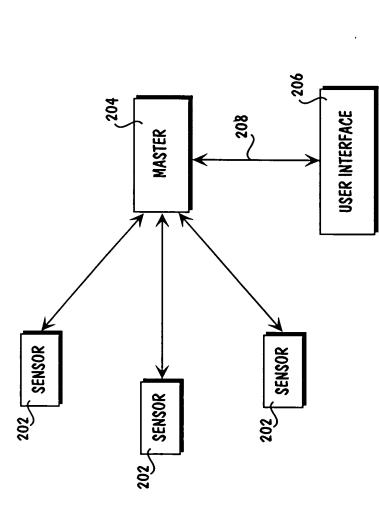


FIG. 1 (Prior Art)



 \mathcal{N}_{8}

FIG. 2 (Prior Art)

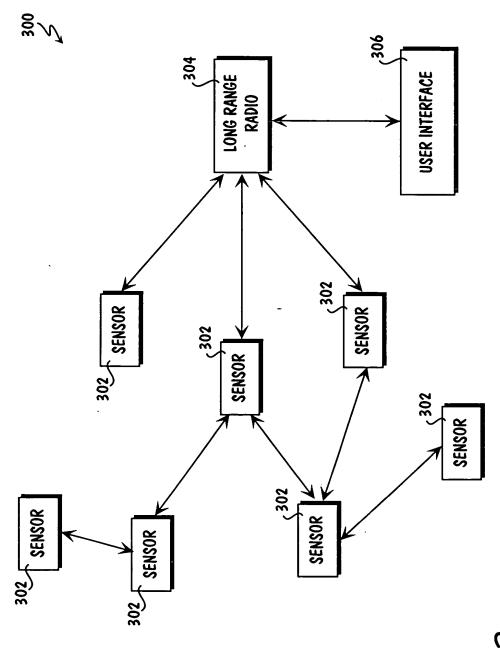


FIG. 3 (Prior Art)

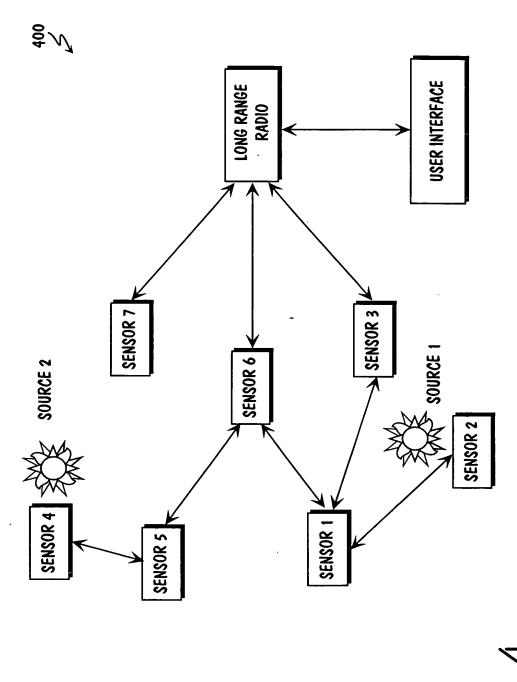


FIG. 4 (Prior Art)

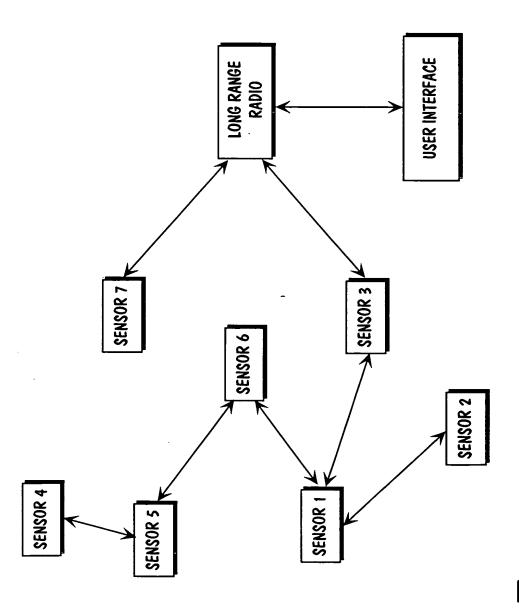
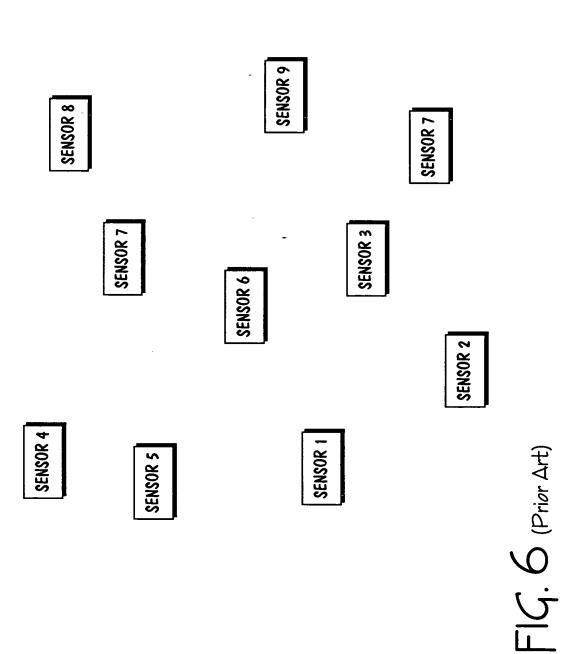


FIG. 5 (Prior Art)



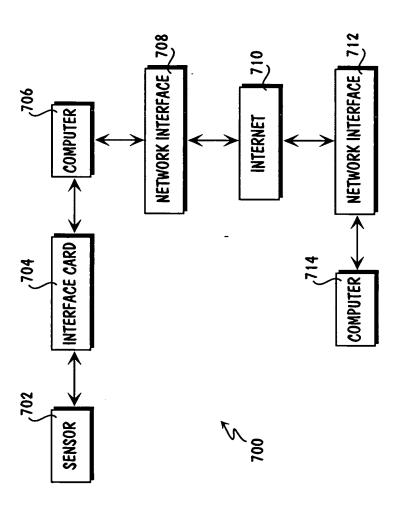
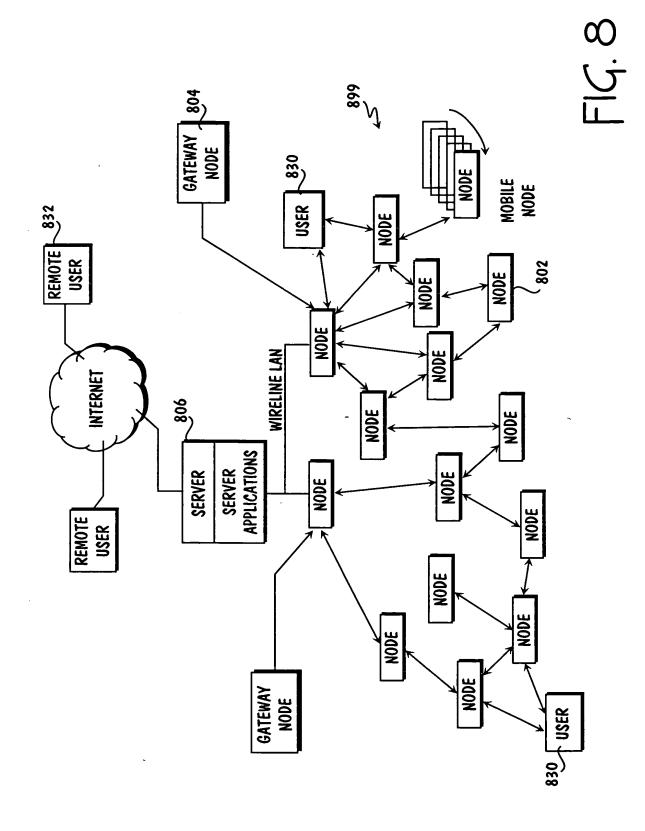
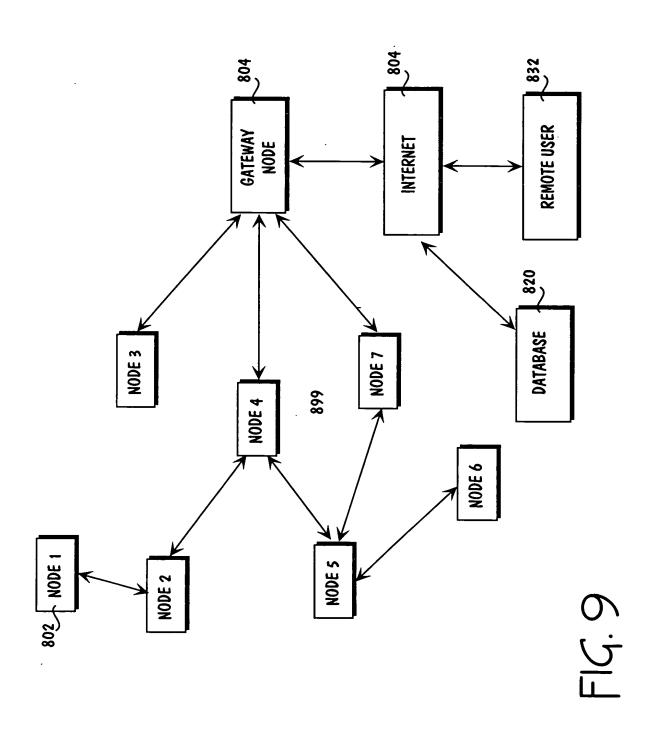


FIG. 7 (Prior Art)





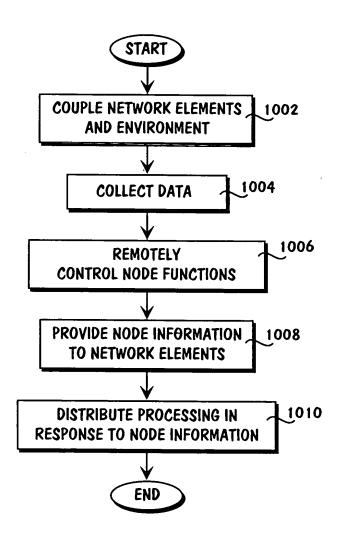
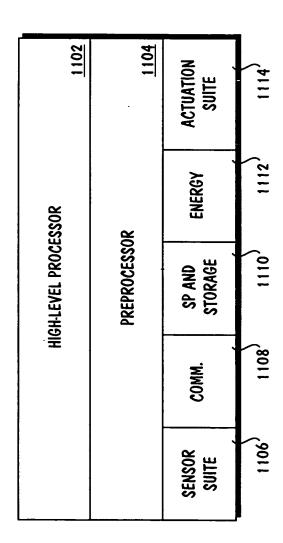


FIG. 10

PROCESSING WITHIN NODES

 $\frac{1}{6}\mathcal{N}$



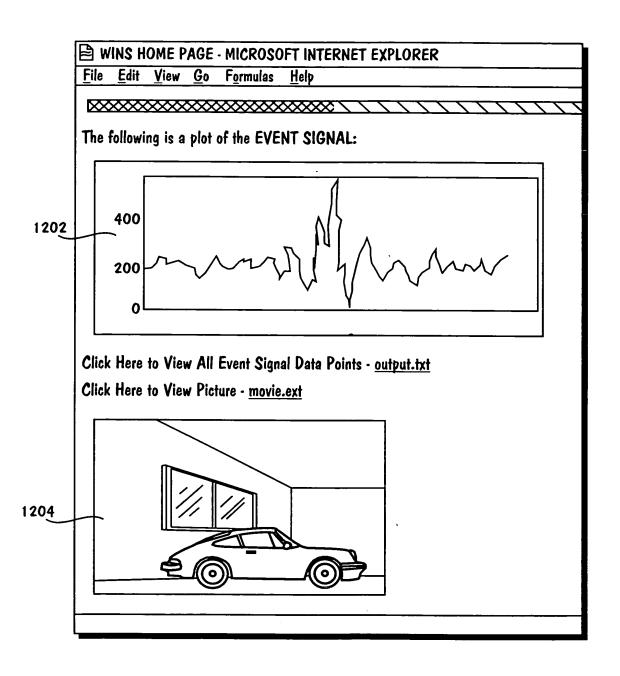


FIG. 12

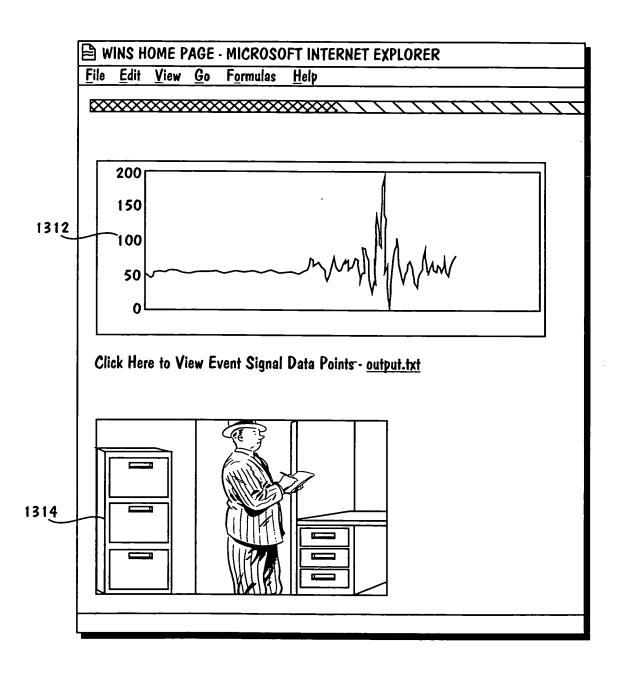


FIG. 13

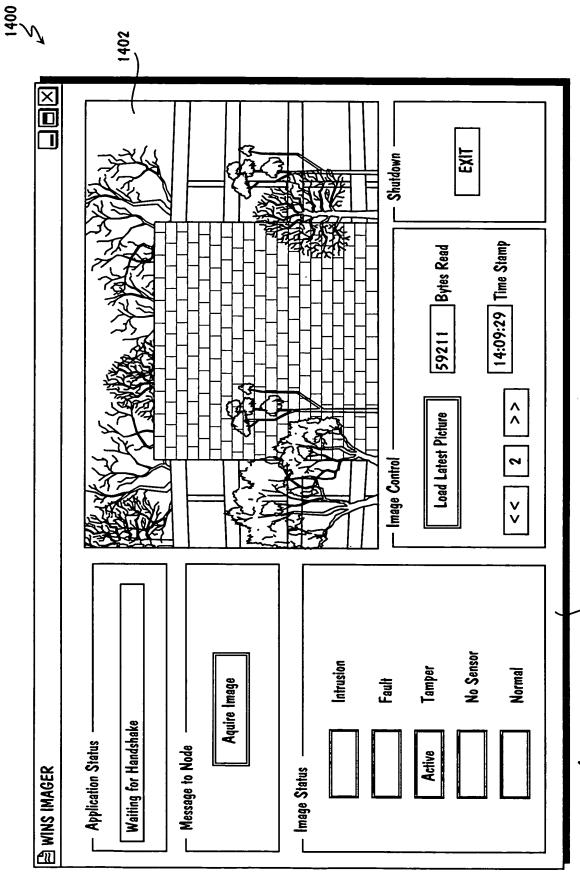


FIG. 14

1404

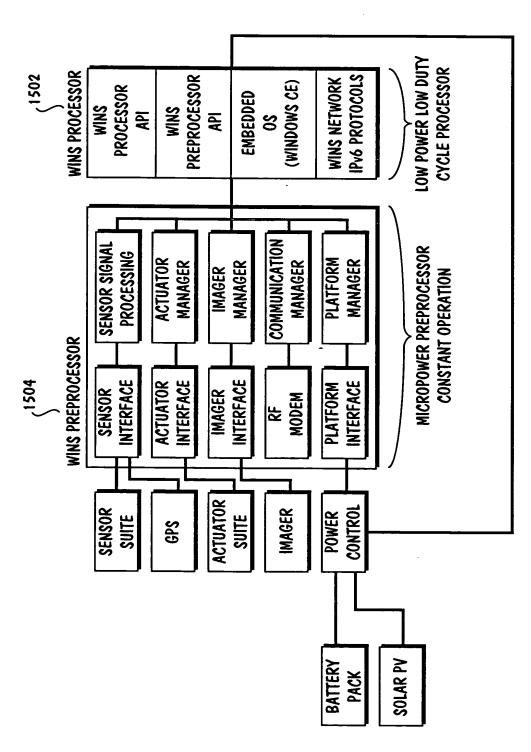


FIG. 15

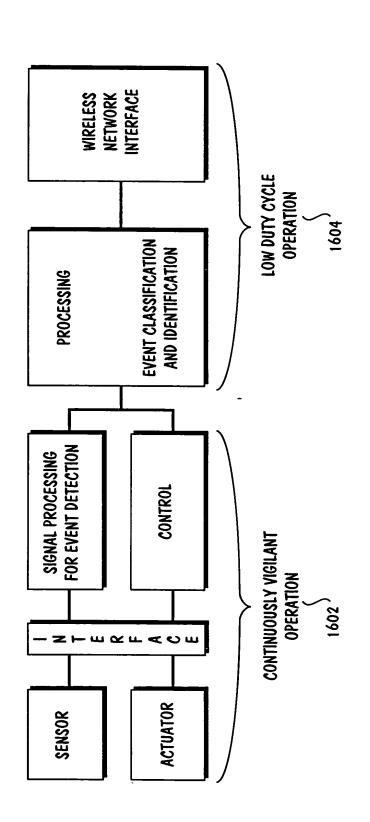


FIG. 16

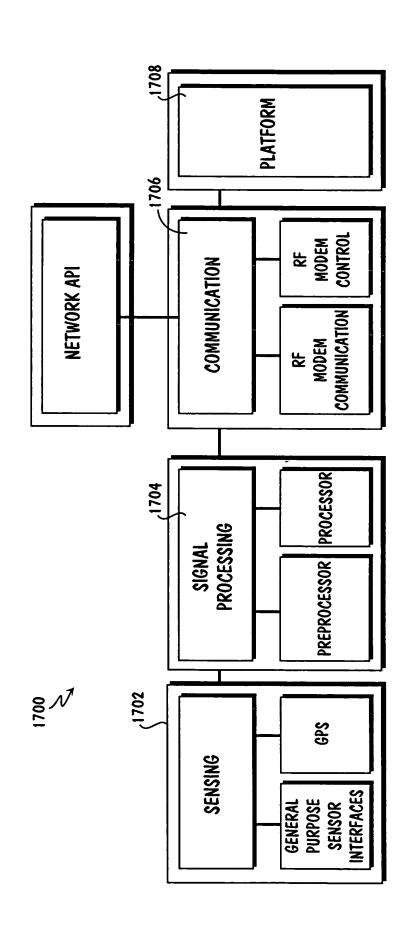


FIG. 17

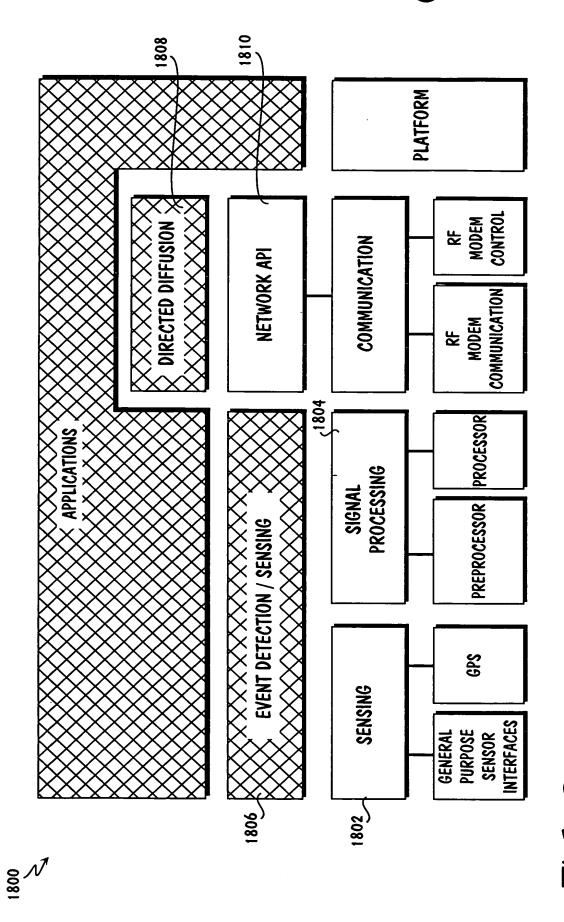


FIG. 18

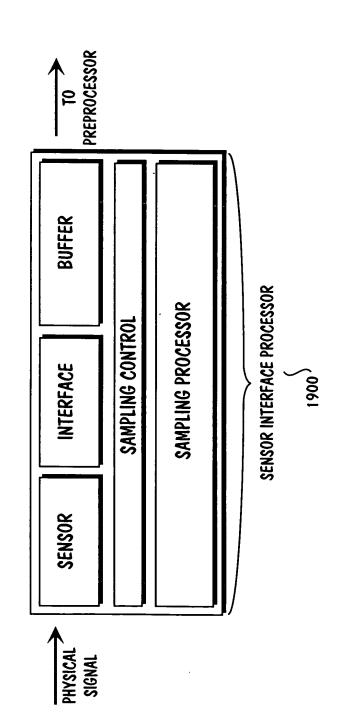


FIG. 19

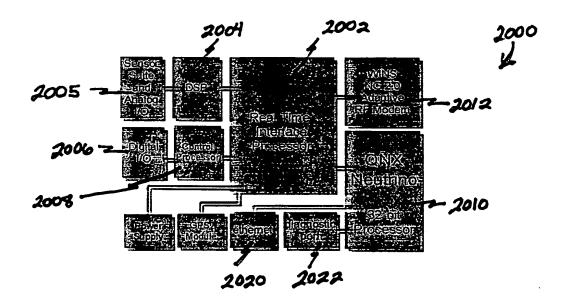
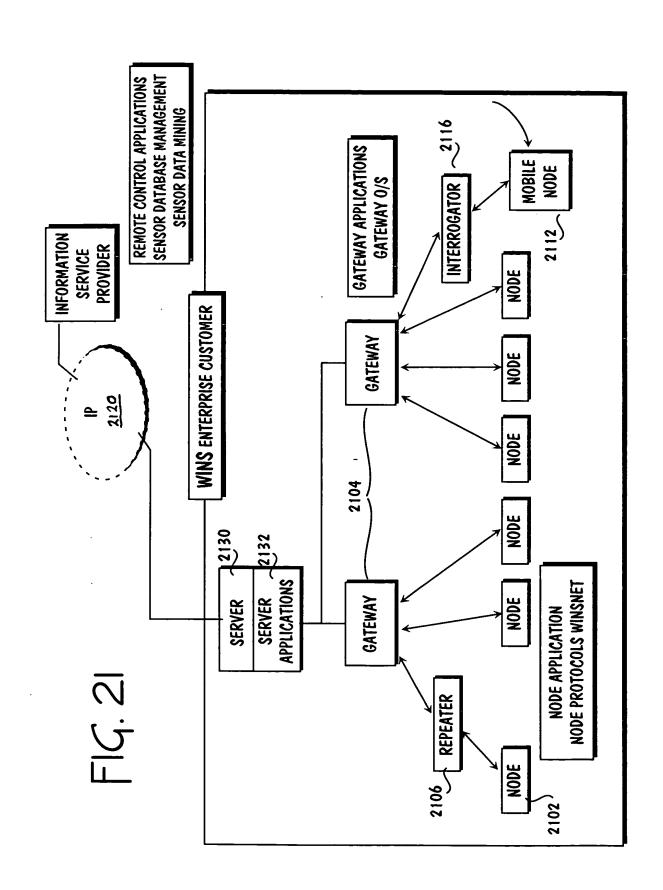


FIGURE 20



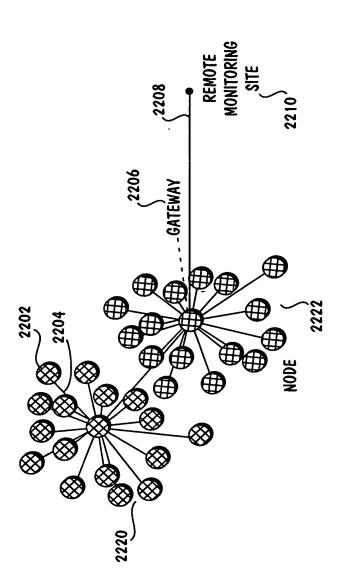


FIG. 22

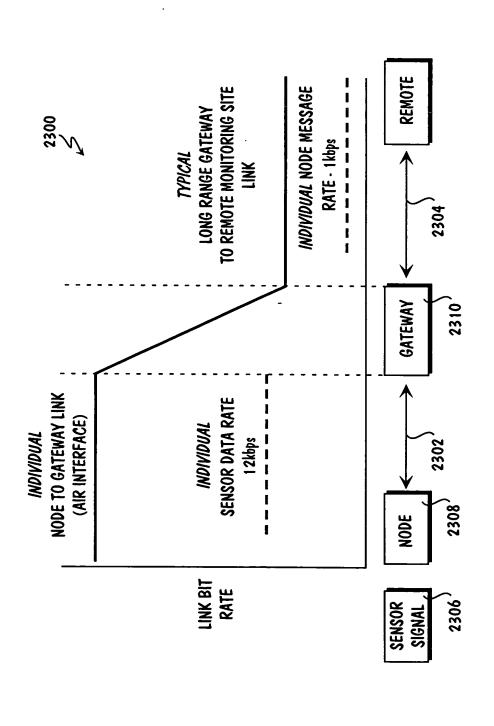
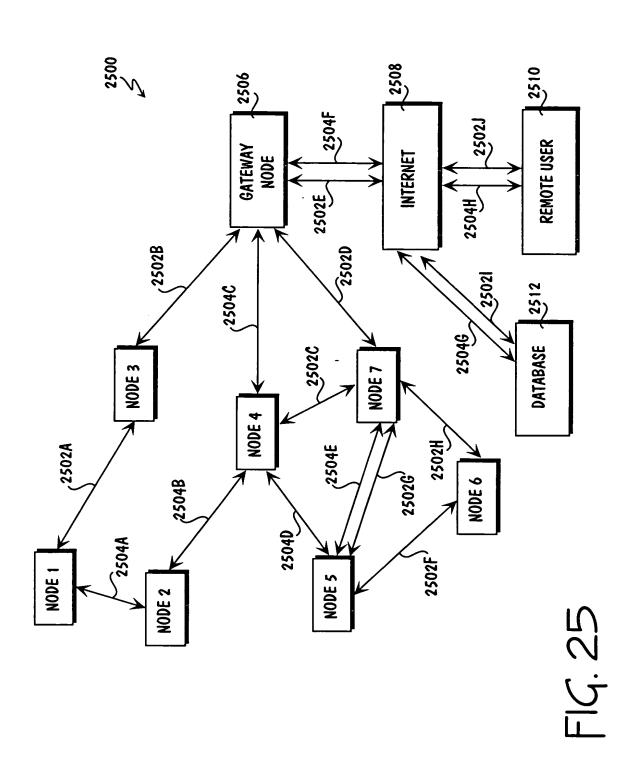


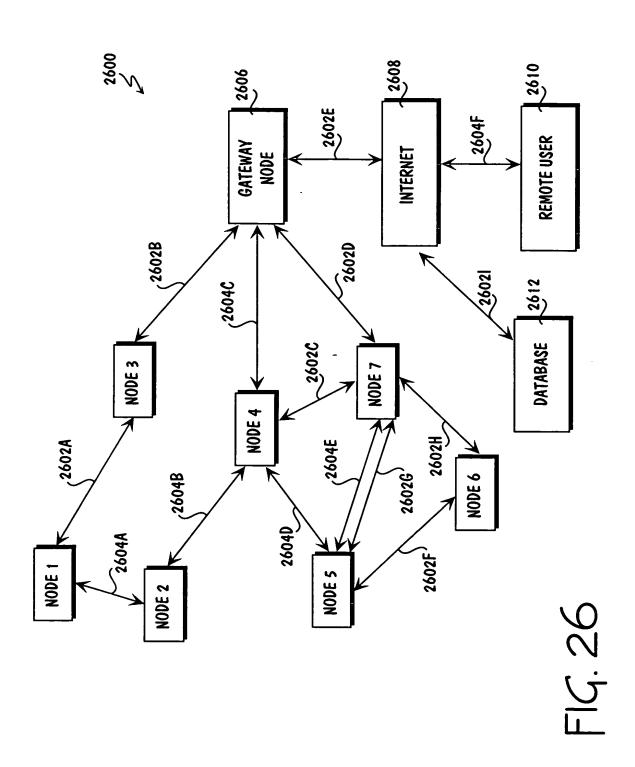
FIG. 23

Z400

DESTINATION SOURCE DATA ADDRESS ADDRESS (UP TO 2048 BYTES)	N SOURCE ADDRESS
DESTINATION SOURCE ADDRESS ADDRESS	DESTINATION SOURCE ADDRESS ADDRESS
	LENGTH (HIGH ORDER BYTE)

FIG. 24





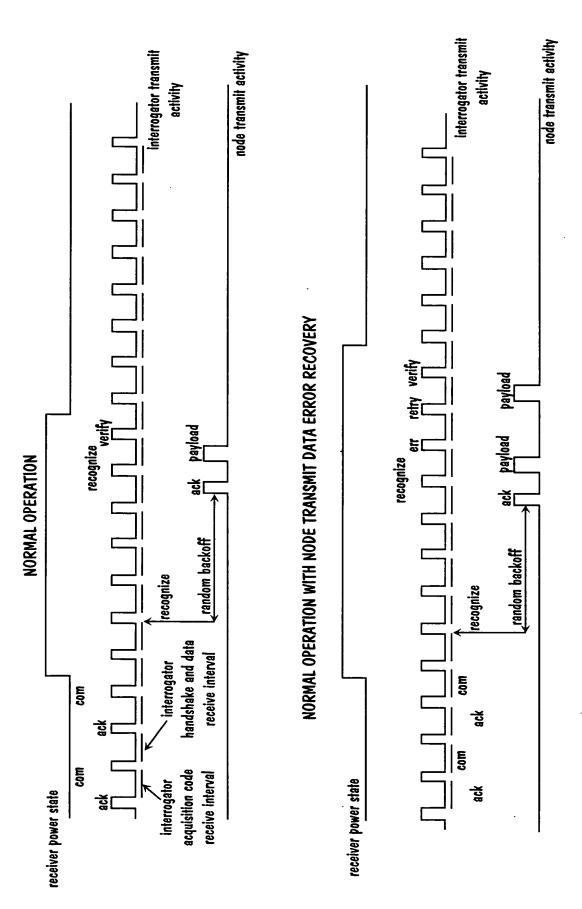
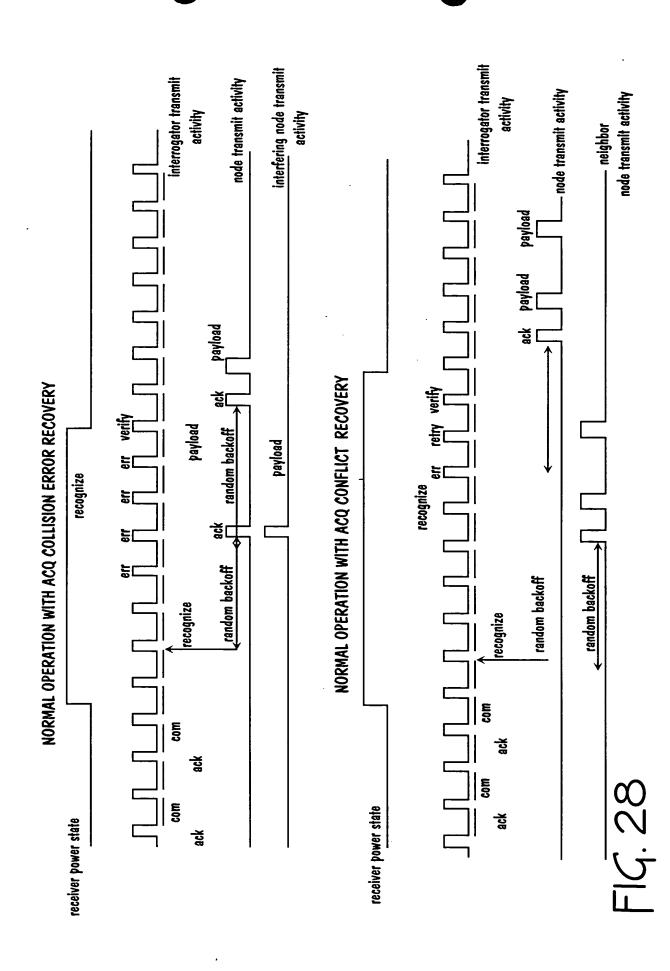


FIG. 27



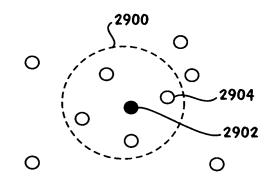


FIG. 29

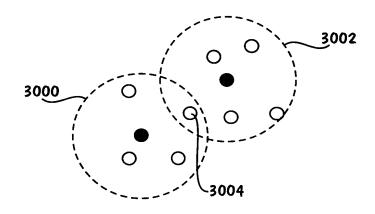


FIG. 30

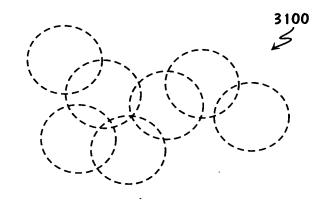


FIG. 31

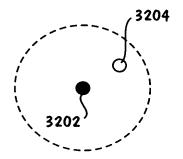


FIG. 32

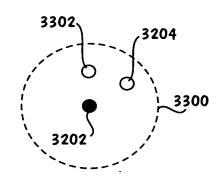


FIG. 33

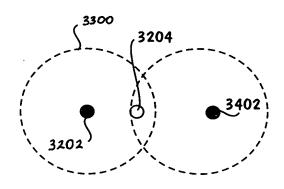
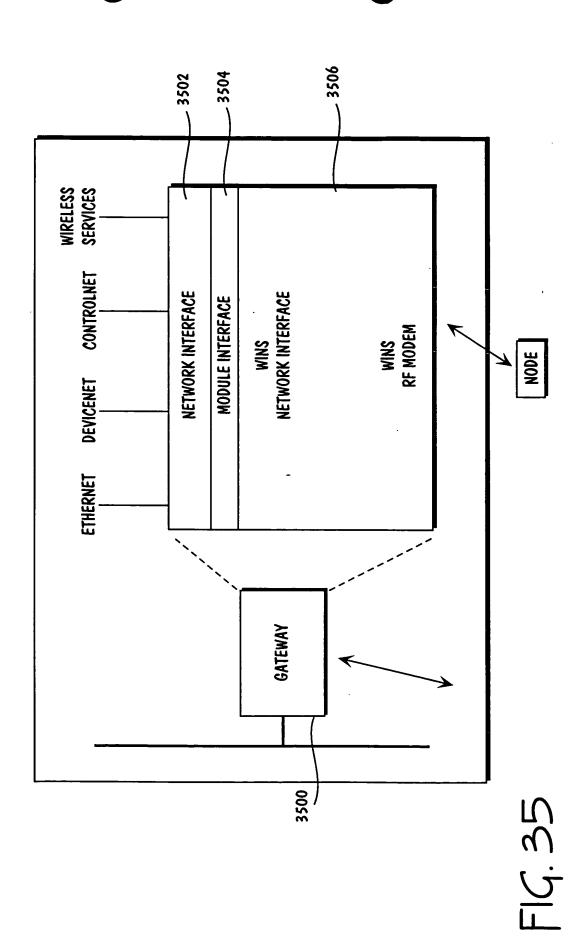
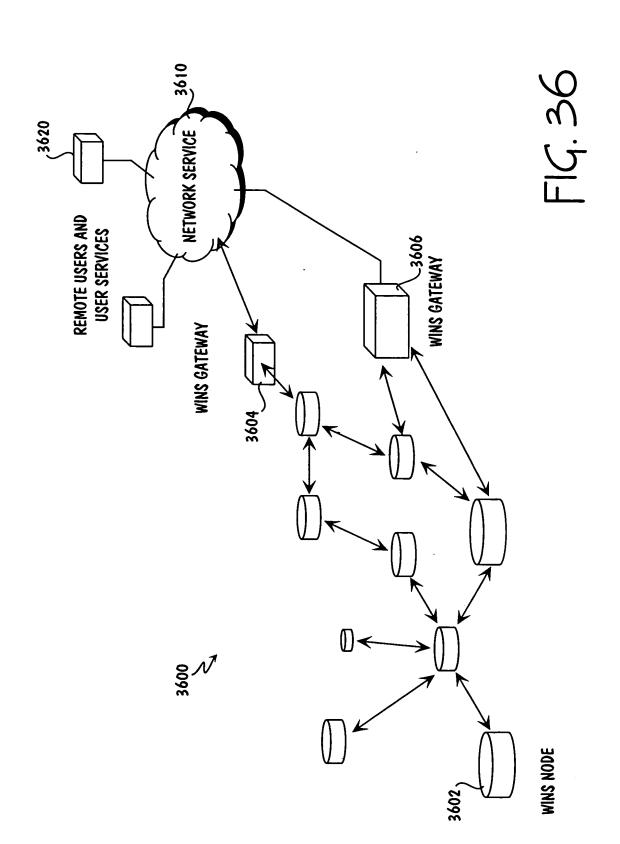


FIG. 34





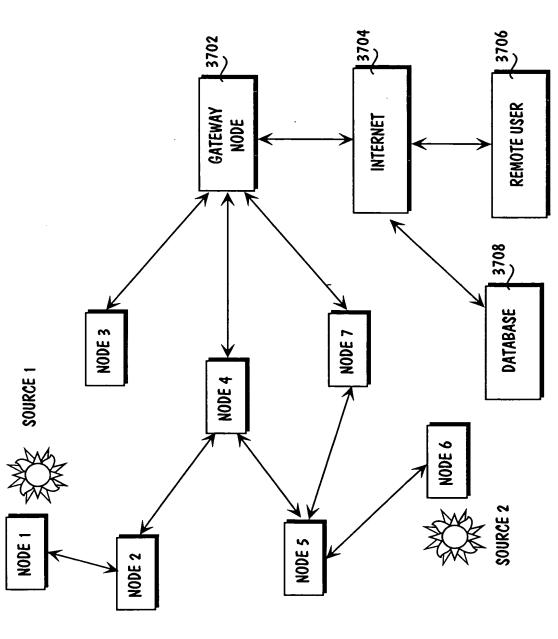


FIG. 37

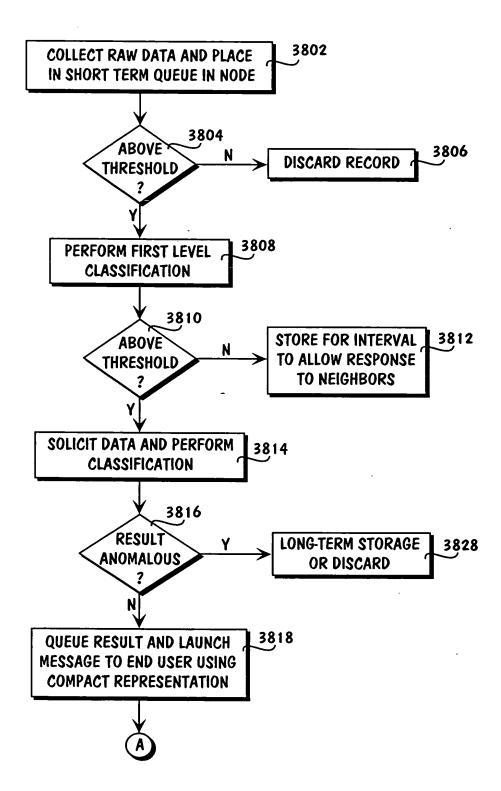


FIG. 38A

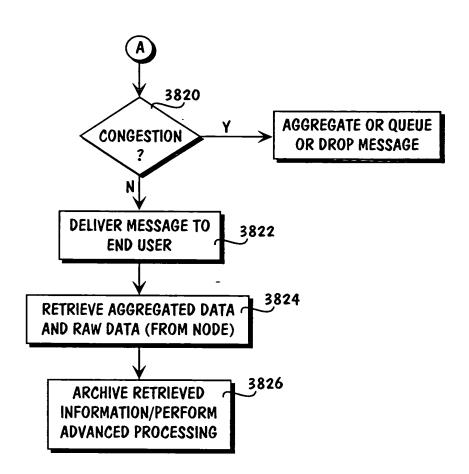
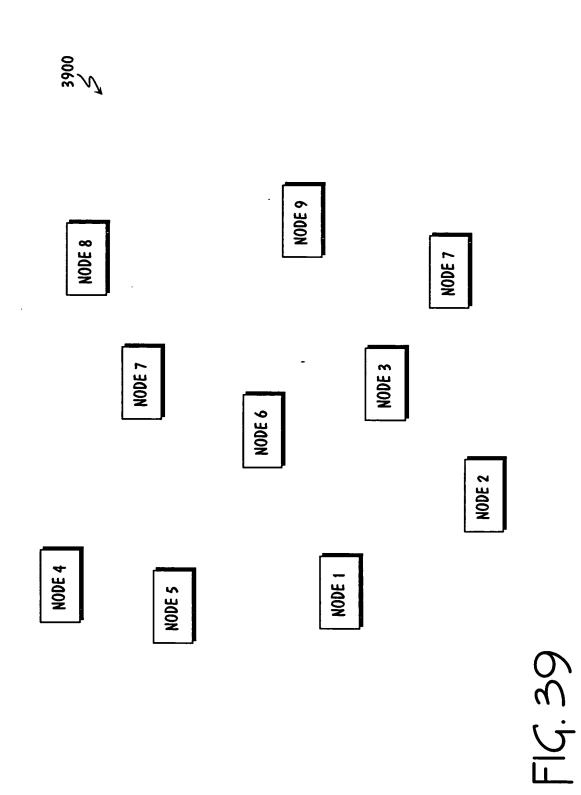
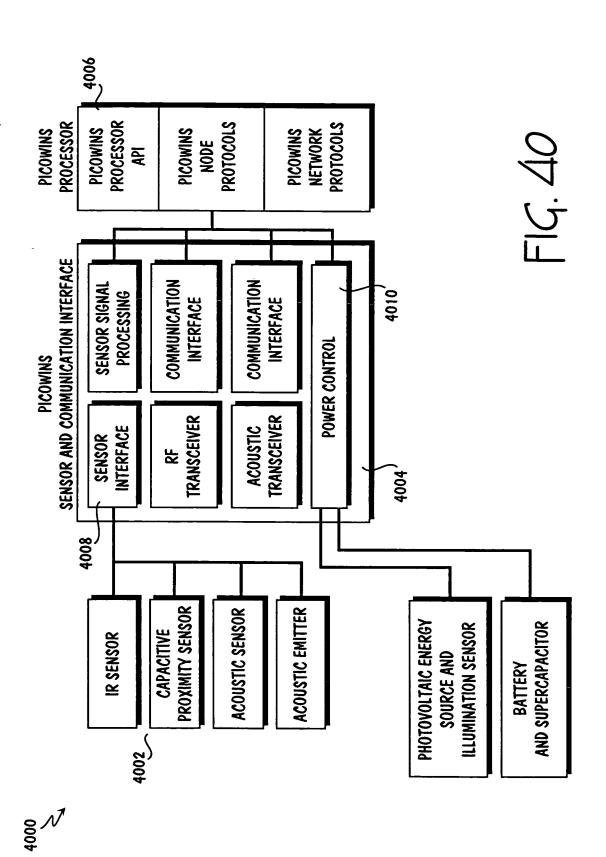
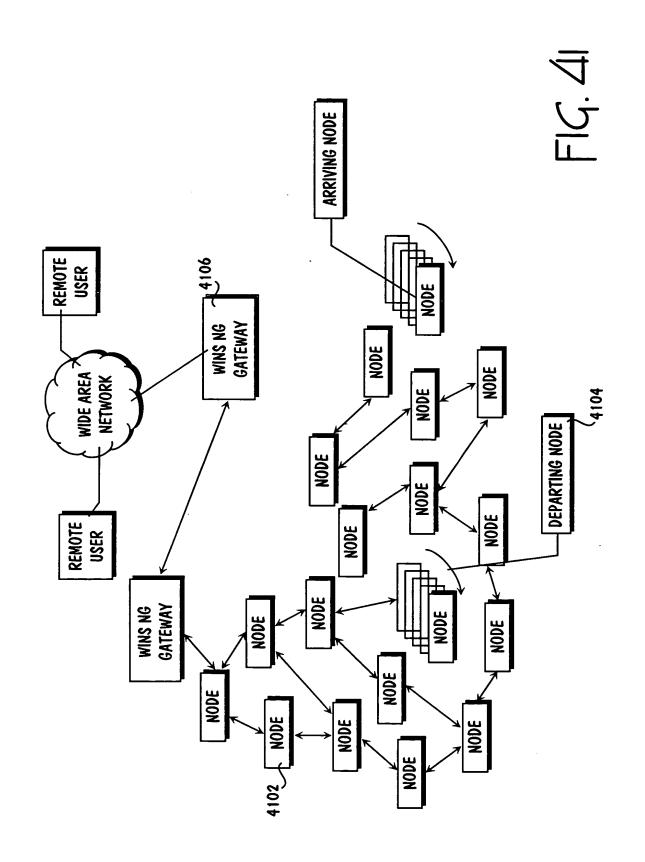


FIG. 38B







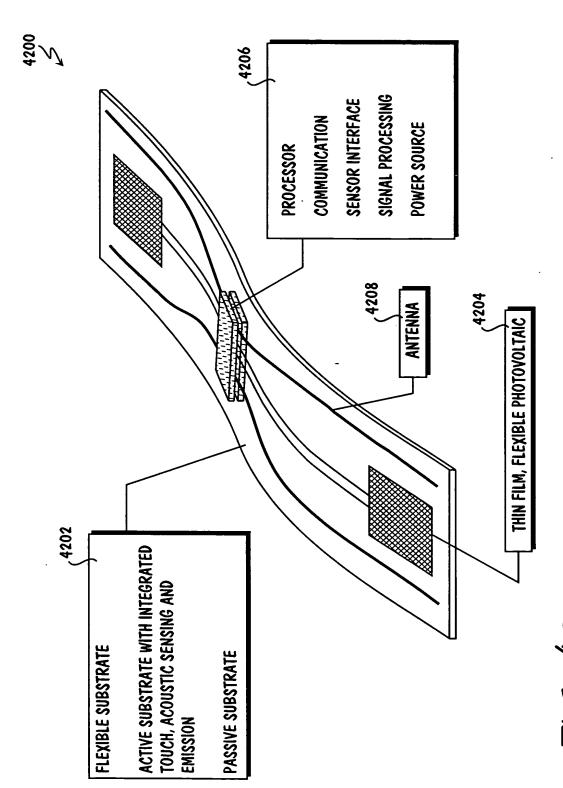
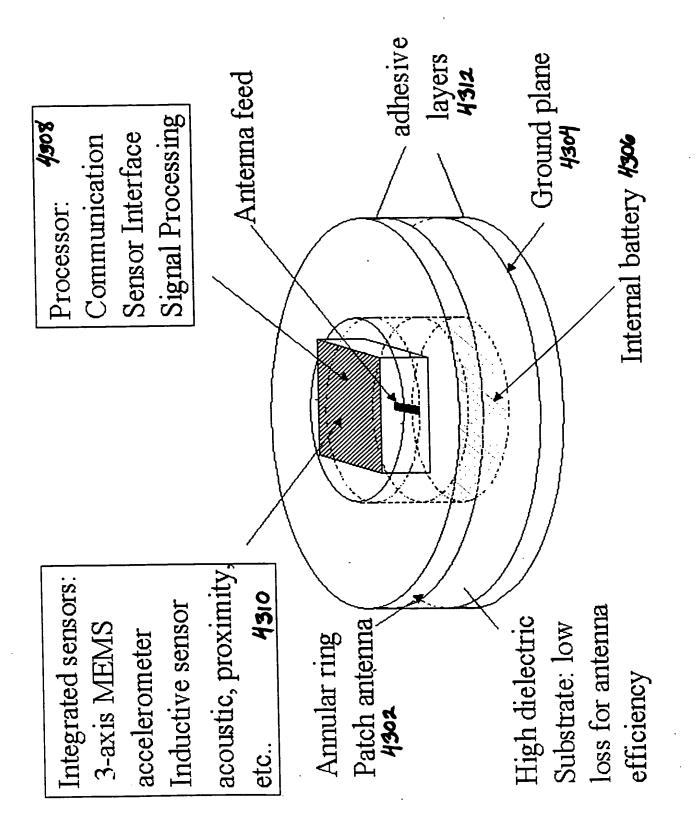
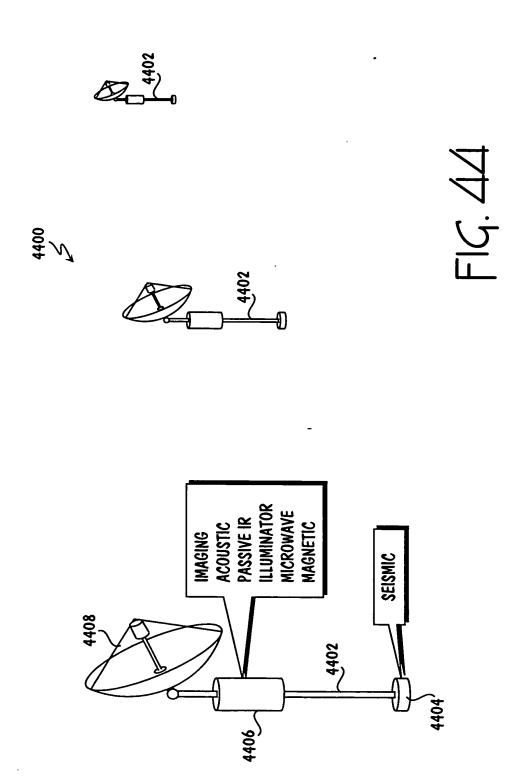


FIG. 42



Floure 43



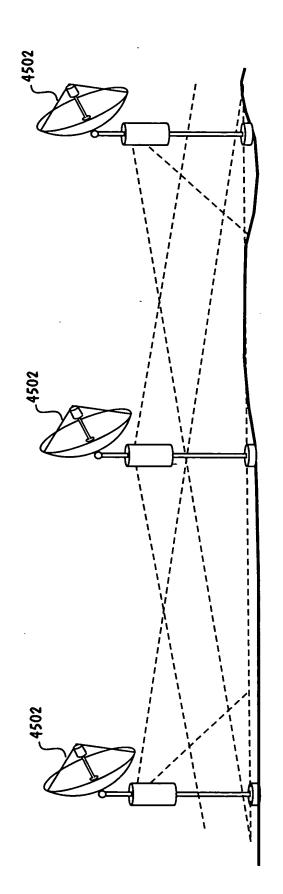


FIG. 45

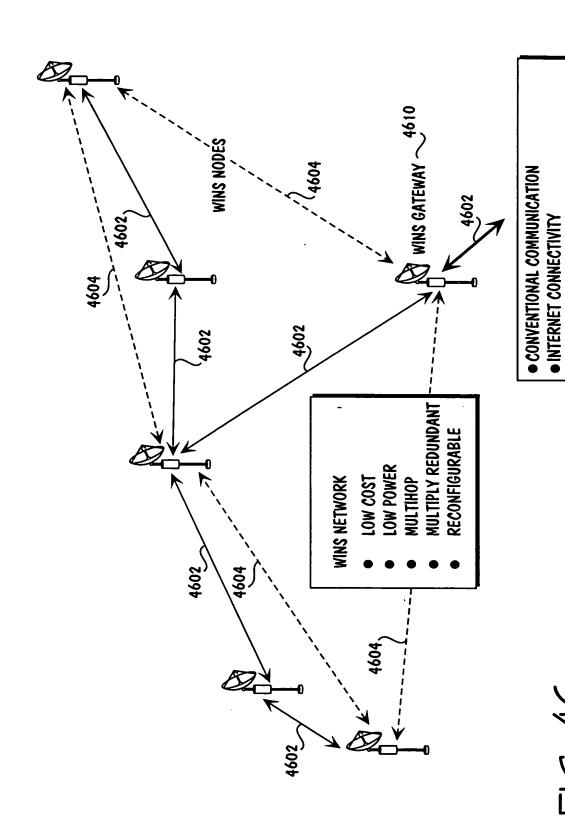
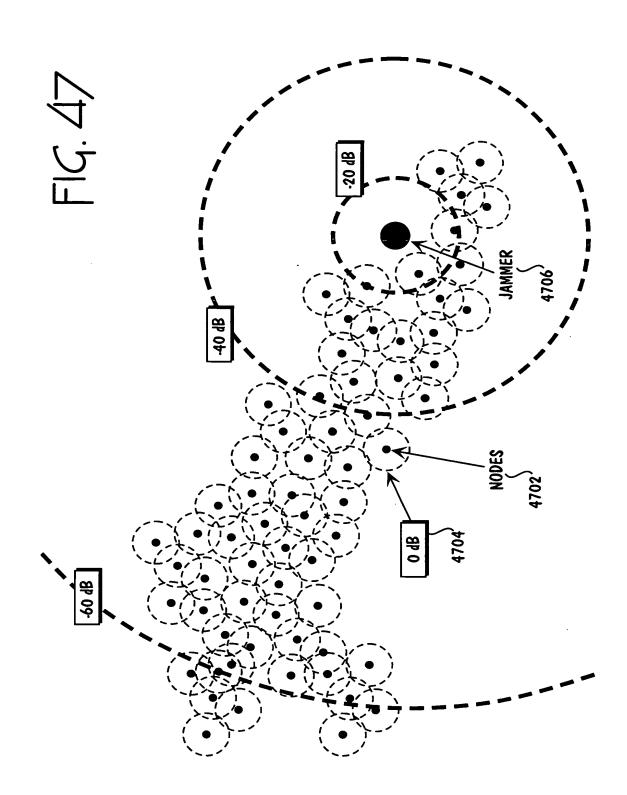
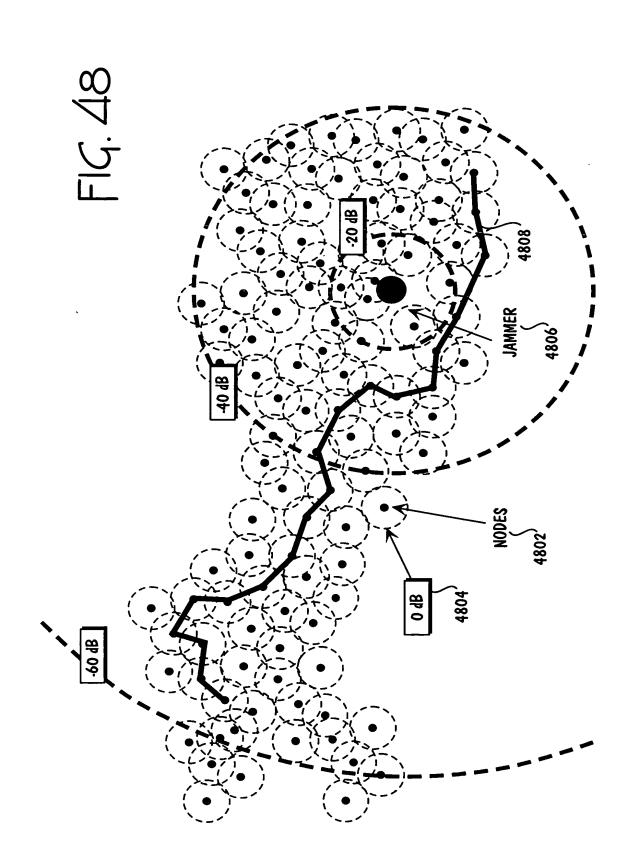


FIG. 46

REMOTE MAINTENANCE AND RECONFIGURABILITY





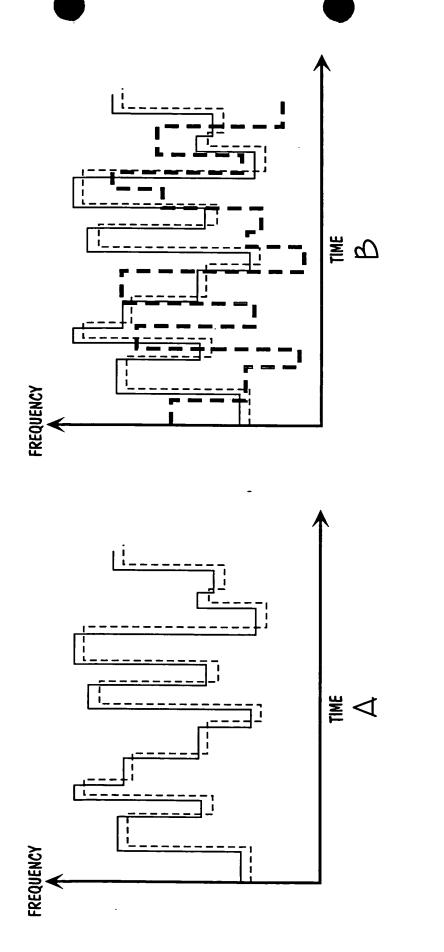
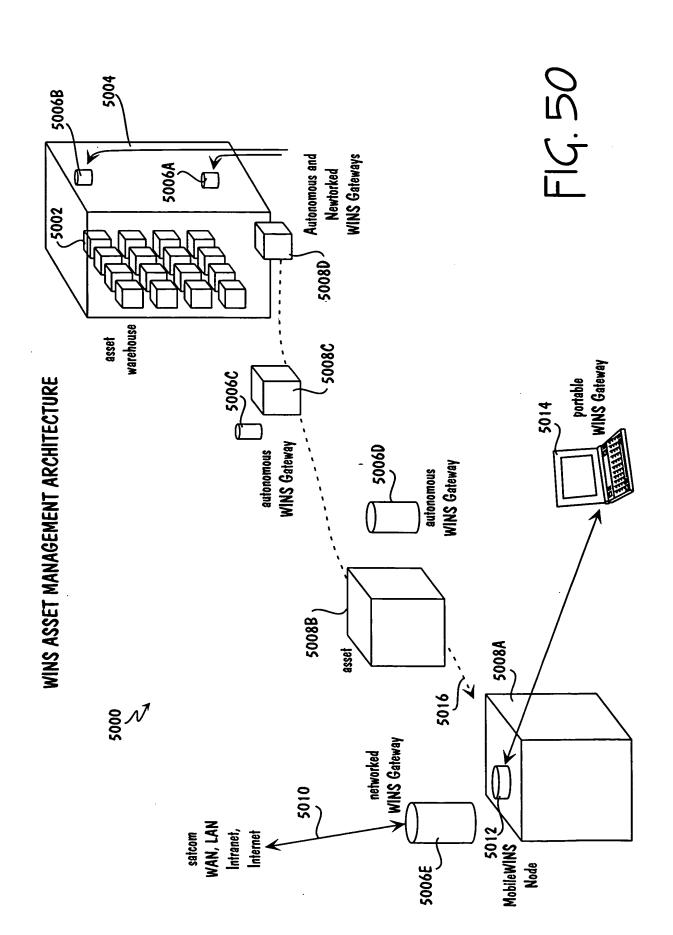


FIG. 49



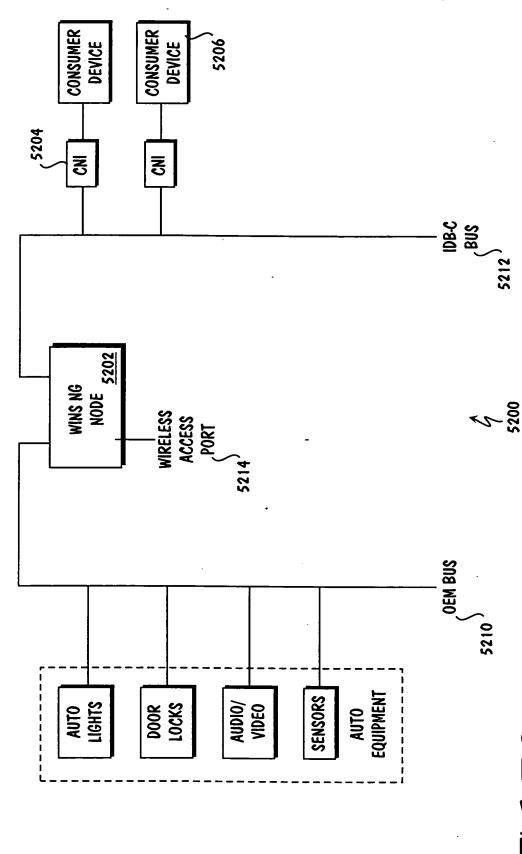


FIG. 52

